



CAT6 UTP RJ45 Patch Panels



Applications

The Vericom Category 6 UTP RJ45 Patch Panels support 1000BASE-T (Gigabit Ethernet), 100BASE-T (Fast Ethernet), 155 Mb/s ATM, 622 Mb/s ATM, 1.2 Gb/s ATM, Digital video and broadband/baseband analog video, Voice over Internet Protocol (VoIP) and is backwards compatible with Category 5e systems. Compatible with PoE applications.

Description / Specifications

The Vericom Category 6 UTP RJ45 Patch Panels are designed to meet ANSI/TIA-568 C.2 Category 6 and ISO 11801 electrical performance requirements for frequencies up to 250 MHz. The Panels are available in 24 Port (1 RU) and 48 Port (2 RU) with a standard RJ-45 interface for patching to active devices or cross connects, and a standard 110 punchdown for terminating horizontal cable.

Features & Benefits

- Vericom Patch Panels support all Category 6 applications.
- Contacts are gold plated.
- Universal label shows T568A and T568B wiring for ease of use.
- 110 IDCs are compatible with a standard punchdown tool.
- Write on labels are provided.
- Supports PoE/PoE+.

Standards Compliance

- RoHS Compliant
- ISO/IEC 11801
- TIA/EIA-568
- Supports PoE applications

Related Products

Deluxe 66/110 Punch Down Tool

Item No. WTPDT-03633





CAT6 UTP RJ45 Patch Panels

Ordering Information

Description			Also Available:		
CAT6 UTP RJ45 Patch Panel			CAT6 Keystone Pass-Through 24-Port Patch Panel		
Item Number	Number of Ports	Rack Unit Quantity	Item Number	Number of Ports	Rack Unit Quantity
MPP6U-01361	24	1RU	MPP6U-01363*	24 Pass-Through Ports	1RU
MPP6U-01362	48	2RU			

* (Not Pictured / Illustrated)

Construction

Housing: ABS

IDC: 110 type, phosphor bronze alloy with tin plating alloy

Contacts: Phosphor bronze alloy plated with gold over of nickel plating

Mechanical

Plug Retention: 50 N (11 lbf)

Wire Compatibility: 22 to 26 AWG (0.644 to 0.511 mm)

Mating Life Cycle: Min 750 cycles

Environmental Conditions

Operating Temperature: 14 °F to 140 °F (-10 °C to 60 °C)

Storage Temperature: -40 °F to 158 °F (-40 °C to 70 °C)

Technical Illustration

